



LC420WUF

Product Specification

SPECIFICATION FOR APPROVAL

() Preliminary Specification

(●) Final Specification

Title	42.0" WUXGA TFT LCD
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BUYER	General
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC420WUF
SUFFIX	SSA1 (RoHS Verified)

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE	DATE
/		
/		
/		

Please return 1 copy for your confirmation with
your signature and comments.

APPROVED BY	SIGNATURE	DATE
S.C.Won /Senior Manager		
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PREPARED BY		
K.S. Moon / Engineer		

TV Product Development Dept.
LG. Philips LCD Co., Ltd



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RECORD OF REVISIONS

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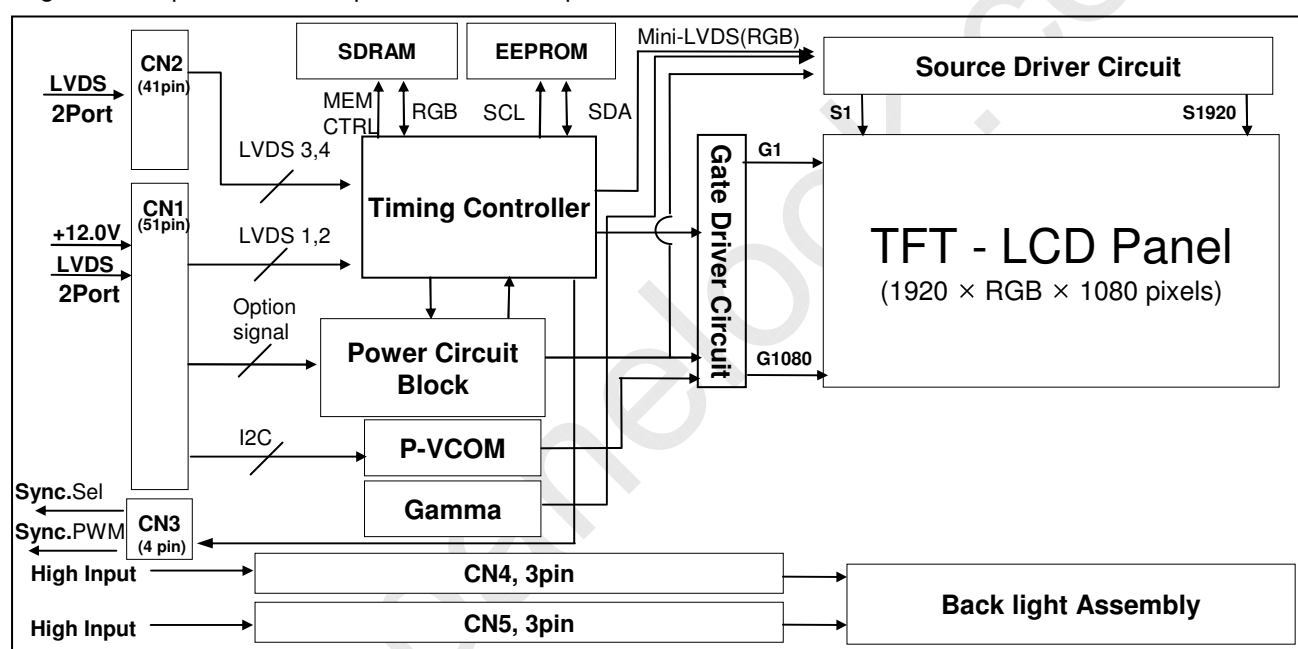
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1. General Description

The LC420WUF is a Color Active Matrix Liquid Crystal Display with an Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 42 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot, thus presenting a palette of more than 1.06Billion (10bit(D)) of colors.

It has been designed to apply the 10-bit 4 port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	42.02 inches(1067.31mm) diagonal
Outline Dimension	983.0 mm(H) x 576.0 mm(V) x 29.3 mm(D) (Typ.)
Pixel Pitch	484.5 μ m x 484.5 μ m
Pixel Format	1920 horiz. by 1080 vert. pixels RGB stripe arrangement
Color Depth	10bit(D), 1.06Billion colors
Luminance, White	450 cd/m ² (Center 1 point Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 187.74 Watt (Typ.) (Logic=7.74 W, Inverter = 180 W [IBL=140mA])
Weight	9.8 Kg (Typ.)
Display Operating Mode	transmissive mode, normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer

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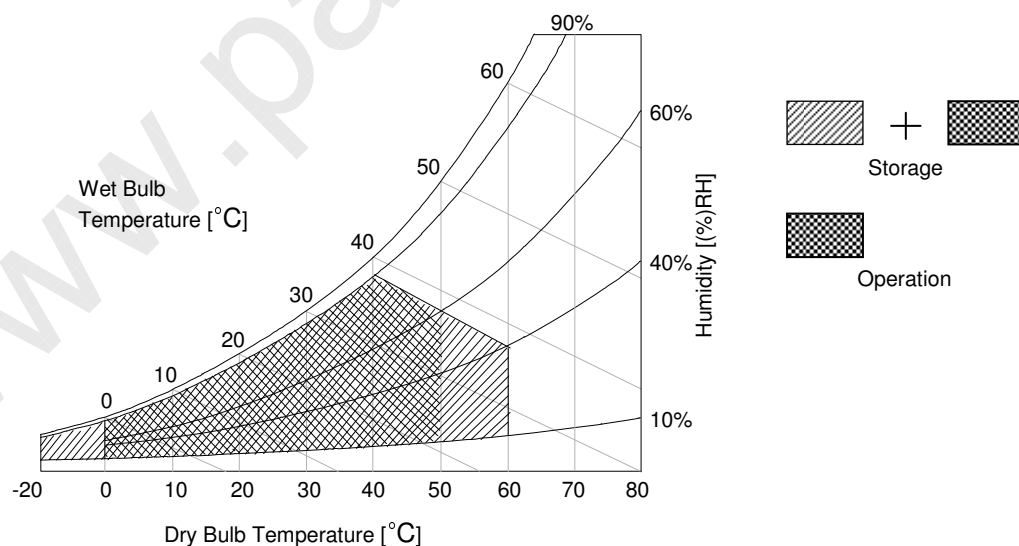
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Remark
			Min	Max		
Power Input Voltage	LCD circuit	V _{LCD}	-0.3	+14.0	V [DC]	at 25 ± 2 °C
B/L Input voltage	Operating Voltage (one side)	V _{OP}	1000	2000	V[RMS]	at 25 ± 2 °C Burst Dimming Duty 100%
Operating Temperature		T _{OP}	0	+50	°C	Note 1
Storage Temperature		T _{ST}	-20	+60	°C	
Operating Ambient Humidity		H _{OP}	10	90	%RH	
Storage Humidity		H _{ST}	10	90	%RH	

Notes : 1. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 °C Max. and no condensation of water.
2. Gravity mura can be guaranteed under 40°C condition.



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3. Electrical Specifications

3-1. Electrical Characteristics

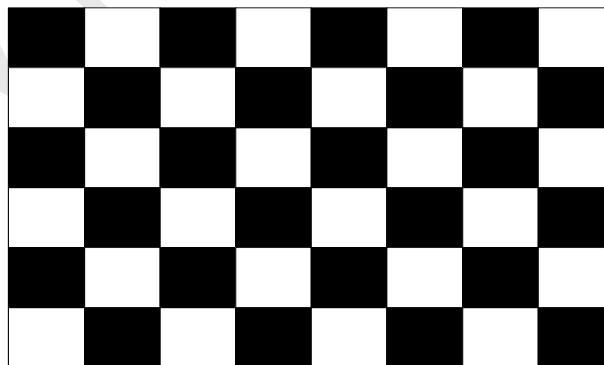
It requires two power inputs. One is employed to power for the LCD circuit. The other is used for the CCFL backlight and inverter circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			Unit	Note
		Min	Typ	Max		
Circuit :						
Power Input Voltage	VLCD	11.4	12.0	12.6	Vdc	
Power Input Current	ILCD	-	645	838	mA	1
		-	1140	1480	mA	2
Power Consumption	PLCD	-	7.74	-	Watt	1
Rush current	IRUSH	-	-	5.0	A	3

- Notes : 1. The specified current and power consumption are under the $V_{LCD}=12.0V$, $25 \pm 2^{\circ}C$, $f_V=120Hz$ condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
2. The current is specified at the maximum current pattern.
3. The duration of rush current is about 2ms and rising time of power input is 1ms (min)

White : 1023Gray
Black : 0Gray



Mosaic Pattern(8 x 6)

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Table 3. ELECTRICAL CHARACTERISTICS for Back Light Assembly & Lamp (Continue)

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
Backlight Assembly :							
Operating Voltage (one side,fBL=45KHz, IBL= 140mArms))		VBL	-	1350	-	V _{RMS}	1, 2
Operating Current (one side)		IBL	-	140	-	mA _{RMS}	1
Established Starting Voltage (one side)	0℃	Vs	-	-	1550	V _{RMS}	1, 3
	25℃		-	-	1410		
Operating Frequency		fBL	43	45	47	kHz	4
Striking Time		S TIME	-	-	1.5	sec	3
Power Consumption		PBL		180		Watt	6
Burst Dimming Duty		PWM duty	20		100	%	9
Burst Dimming Frequency		1/T	150	-	180	Hz	9

Parameter		Symbol	Values			Unit	Notes
			Min	Typ	Max		
Lamp : APPENDIX-V							
Lamp Voltage (one side)		VLAMP	700	745	775	V _{RMS}	2
Lamp Current (one side)		ILAMP	3	6	8	mA _{RMS}	
Discharge Stabilization Time		Ts	-	-	3	Min	5
Lamp Frequency		f LAMP	40	48	80	KHz	
Lamp Temperature		TLAMP			130	℃	
Established Starting Voltage (one side)	0℃	Vs			1050	V _{RMS}	3
	25℃	Vs			910		
Life Time			50,000			Hrs	7

Notes : The design of the inverter must have specifications for the lamp in LCD Assembly.

The electrical characteristics of inverter are based on High-High Driving type.

The performance of the lamps in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So, all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD- Assembly should be operated in the same condition as installed in your instrument.

※ Do not attach a conductive tape to lamp connecting wire.

If you attach conductive tape to the lamp wire, not only luminance level can be lower than typical one but also inverter operate abnormally on account of leakage current which is generated between lamp wire and conductive tape.

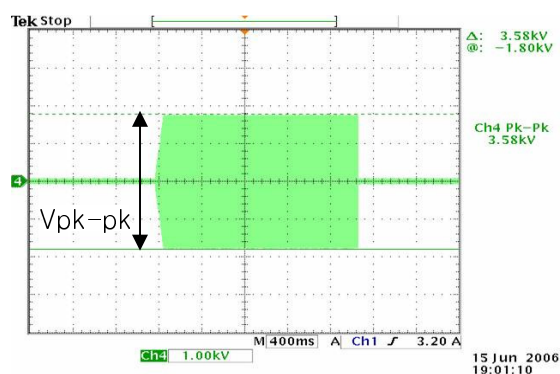
1. Specified values are defined for a Backlight Assembly.(IBL : 20 lamp, 7.0mA/Lamp)

2. Operating voltage is measured at $25 \pm 2^\circ\text{C}$ (after 2hr.aging). The variance range for operating voltage is $\pm 10\%$.

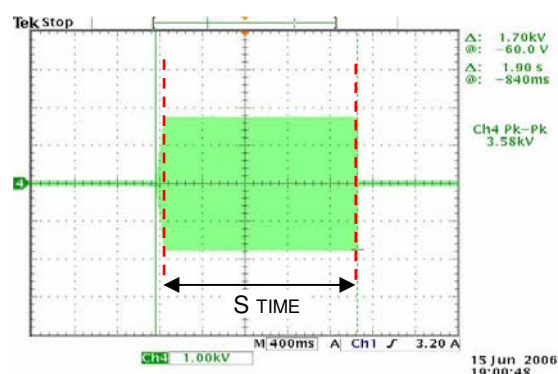
3. The established starting voltage [Vs] should be applied to the lamps for more than Striking time (S TIME) for start-up. Inverter open voltage must be more than established starting voltage. Otherwise, the lamps may not be turned on. The used lamp current is typical value.

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$$V_s = (V_{pk-pk}) / [2 \cdot \sqrt{2}]$$



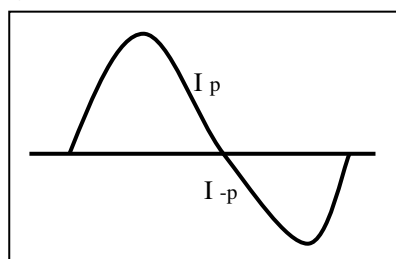
4. Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore, lamp frequency shall be as away possible from the horizontal synchronous frequency and its harmonics range in order to prevent interference.
5. The brightness of the lamp after lighted for 5minutes is defined as 100%.
 T_s is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
 The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on.
6. Maximum level of power consumption is measured at initial turn on.
 Typical level of power consumption is measured after 2hrs aging at $25 \pm 2^\circ\text{C}$.
7. The life time is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^\circ\text{C}$, based on duty 100%.
8. The output of the inverter must have symmetrical(negative and positive) voltage and current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has not only unsymmetrical voltage and current but also spike wave.

Requirements for a system inverter design, which is intended to achieve better display performance, power efficiency and more reliable lamp characteristics.

It can help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within $\sqrt{2} \pm 10\%$.

* Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate:

$$|I_p - I_{-p}| / I_{op} \times 100\%$$

* Distortion rate

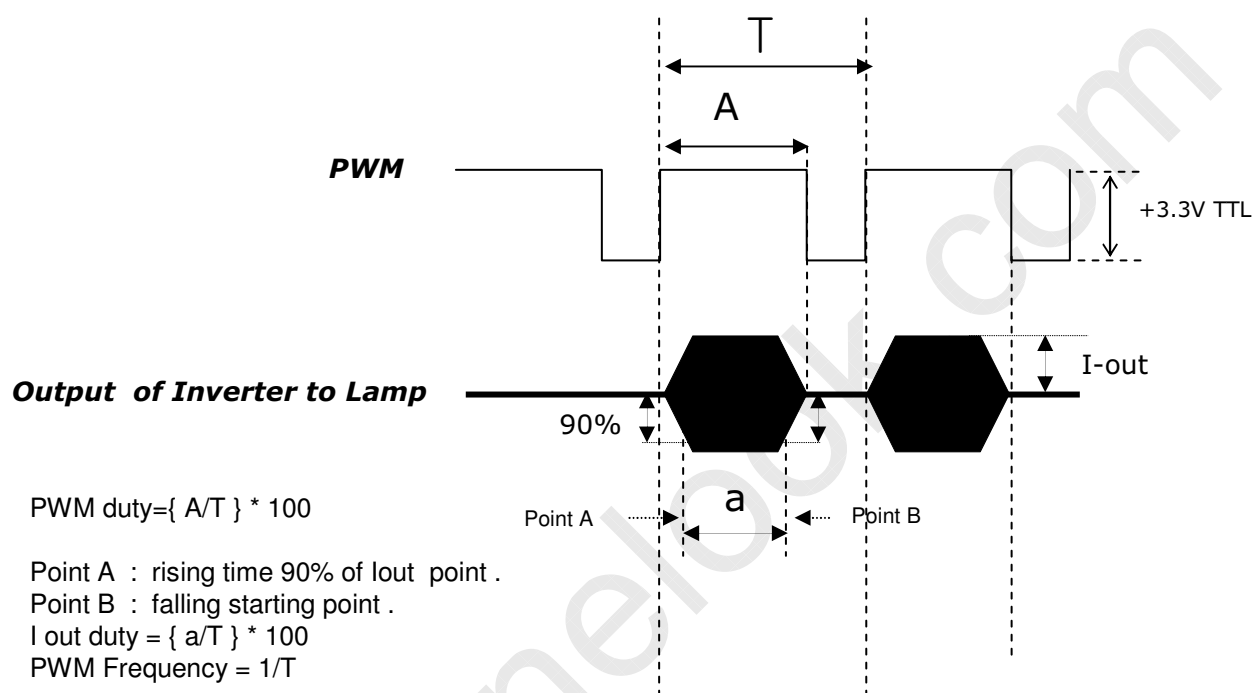
$$I_p \text{ (or } I_{-p}) / I_{op}$$

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9. The reference method of burst dimming duty ratio.

It is recommended to use synchronous V-sync frequency for prevent waterfall
(Vsync x 1.5 =Burst Frequency)



※ We recommend not to be much different between PWM duty and Iout duty .

※ Dimming current output rising and falling time may produce humming and inverter trans' sound noise.

※ Burst dimming duty should be 100% for 1second after turn on

※ Equipment

Oscilloscope :TDS3054B(Tektronix)

Current Probe : P6022 AC (Tektronix)

High Voltage Probe: P5100(Tektronix)

10. The Cable between the backlight connector and its inverter power supply should be connected directly with a minimized length. The longer cable between the backlight and the inverter may cause the lower luminance of lamp and may require more higher starting voltage (Vs).

11. The operating current must be measured as near as backlight assembly input.

12. The operating current unbalance between left and right must be under typ $\pm 10\%$.



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3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51/41-pin connectors are used for the module electronics and 14-pin connector & 12 pin connector are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1,CN2): FI-RE51S-HF or Equivalent, Refer to below and next Page Table
- Mating Connector : FI-RE51HL

Table 3-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	GND	Ground(Reserved)
2	NC	Reserved	28	RA2N	SECOND CHANNEL A-
3	NC	Reserved	29	RA2P	SECOND CHANNEL A+
4	NC	Reserved	30	RB2N	SECOND CHANNEL B-
5	NC	Reserved	31	RB2P	SECOND CHANNEL B+
6	NC	Reserved	32	RC2N	SECOND CHANNEL C-
7	LVDS Select	'L' :NS / 'H' :JEIDA	33	RC2P	SECOND CHANNEL C+
8	NC	Reserved	34	GND	Ground
9	NC	Reserved	35	RCLK2N	SECOND CLOCK CHANNEL Clk-
10	NC	Reserved	36	RCLK2P	SECOND CLOCK CHANNEL Clk+
11	GND	Ground	37	GND	Ground
12	RA1N	FIRST CHANNEL A-	38	RD2N	SECOND CHANNEL D-
13	RA1P	FIRST CHANNEL A+	39	RD2P	SECOND CHANNEL D+
14	RB1N	FIRST CHANNEL B-	40	RE2N	SECOND CHANNEL E-
15	RB1P	FIRST CHANNEL B+	41	RE2P	SECOND CHANNEL E+
16	RC1N	FIRST CHANNEL C-	42	GND	Ground
17	RC1P	FIRST CHANNEL C+	43	GND	Ground
18	GND	Ground	44	GND	Ground (NSB)
19	RCLK1N	FIRST CLOCK CHANNEL Clk-	45	GND	Ground
20	RCLK1P	FIRST CLOCK CHANNEL Clk+	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	RD1N	FIRST CHANNEL D-	48	VLCD	Power Supply +12.0V
23	RD1P	FIRST CHANNEL D+	49	VLCD	Power Supply +12.0V
24	RE1N	FIRST CHANNEL E-	50	VLCD	Power Supply +12.0V
25	RE1P	FIRST CHANNEL E+	51	VLCD	Power Supply +12.0V
26	GND	Ground	-	-	-

Notes : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

2. All VLCD (power input) pins should be connected together.

3. All Input levels of LVDS signals are based on the EIA 664 Standard.

4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module.

If not used, these pins are no connection.

5. Specific pins(pin No. #8~#9) are used for Inverter test of the LCD module.

If not used, these pins are no connection.

6. Specific pin No. #44 is used for "No signal detection" of system signal interface.

It should be GND for NSB(No Signal Black) during the system interface signal is not.

If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).



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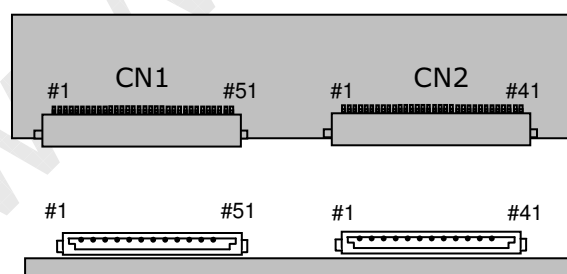
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- LCD Connector(CN2): FI-RE41S-HF, Refer to below table
- Mating Connector : FI-RE41HL

Table 3-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC	No connection(Reserved)	22	RE3N	Third CHANNEL E-
2	NC	No connection	23	RE3P	Third CHANNEL E+
3	NC	No connection	24	GND	Ground
4	NC	No connection	25	GND	Ground
5	NC	No connection	26	RA4N	Fourth CHANNEL A-
6	NC	No connection	27	RA4P	Fourth CHANNEL A+
7	NC	No connection	28	RB4N	Fourth CHANNEL B-
8	NC	No connection	29	RB4P	Fourth CHANNEL B+
9	GND	Ground	30	RC4N	Fourth CHANNEL C-
10	RA3N	Third CHANNEL A-	31	RC4P	Fourth CHANNEL C+
11	RA3P	Third CHANNEL A+	32	GND	Ground
12	RB3N	Third CHANNEL B-	33	RCLK4N	Fourth CLOCK CHANNEL Clk-
13	RB3P	Third CHANNEL B+	34	RCLK4P	Fourth CLOCK CHANNEL Clk+
14	RC3N	Third CHANNEL C-	35	GND	Ground
15	RC3P	Third CHANNEL C+	36	RD4N	Fourth CHANNEL D-
16	GND	Ground	37	RD4P	Fourth CHANNEL D+
17	RCLK3N	Third CLOCK CHANNEL Clk-	38	RE4N	Fourth CHANNEL E-
18	RCLK3P	Third CLOCK CHANNEL Clk+	39	RE4P	Fourth CHANNEL E+
19	GND	Ground	40	GND	Ground
20	RD3N	Third CHANNEL D-	41	GND	Ground
21	RD3P	Third CHANNEL D+	-		

Note : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.



Rear view of LCM

[CN1]

- Part/No. : FI-RE51S-HF(JAE)
- Mating connector : FI-RE51HL
(Manufactured by JAE)

[CN2]

- Part/No. : FI-RE41S-HF(JAE)
- Mating connector : FI-RE41HL
(Manufactured by JAE)

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3-2-2. Backlight Module

[Master]

1) Balance Connector

: 65002WS-03 (manufactured by YEONHO) or equivalent

2) Mating Connector

: 65002HS-03 (manufactured by YEONHO) or equivalent.

[Slave]

1) Balance Connector

: 65002WS-03 (manufactured by YEONHO) or equivalent

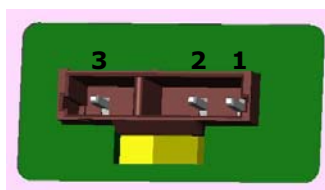
2) Mating Connector

: 65002HS-03 (manufactured by YEONHO) or equivalent.

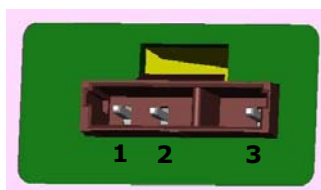
Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN2,CN3)

No	Symbol	Master	Slave	Note
1	H_Input	High_Input	High_Input	
2	H_Input	High_Input	High_Input	
3	FB	NC	NC	

◆ Rear view of LCM



Master



Slave

3-2-3. Sync connection between LCD Module and System

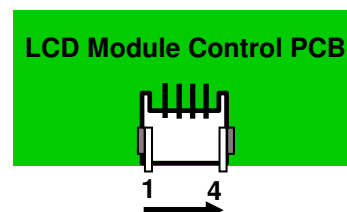
There are 4 pins sync connector in the LCD module control PCB. The No.1 pin is for REFMODE which is selected of Vsync frequency signal. The No.2 pin is for WPWM which is generated for PWM frequency synchronically between control & inverter. The rest pins are for Ground and NC.

1) Inverter Sync Connector

: DF14-4S-1.25C (manufactured by Hirose) or equivalent

Table 6. INVERTER SYNC. CONNECTOR PIN CONFIGURATION(CN4)

Pin No.	Symbol	Description	Note
1	REFMODE	Vsync. Frequency select signal	
2	WPWM	PWM Frequency output	
3	GND	Ground	
4	NC	No connection	



Notes:

1. LPL recommends that Inverter Burst dimming frequency (PWM Frequency) should be synchronized with Sync signal.
2. LPL recommends that Inverter Burst dimming frequency (PWM Frequency) should be 150Hz or 180Hz.

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3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE for NTSC (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	t _{HV}	-	480	-	tclk	1920/4
	Blank	t _{HB}	40	70	200	tclk	
	Total	t _{HP}	520	550	680	tclk	
Vertical	Display Period	t _{VV}	-	1080	-	Lines	
	Blank	t _{VB}	10	45	86	Lines	
	Total	t _{VP}	1090	1125	1166	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	f _{CLK}	70	74.25	75	MHz	
	Horizontal	f _H	121.8	135	136.4	KHz	
	Vertical	f _V	108.2	120	121.2	Hz	

Table 7 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 7. TIMING TABLE for PAL (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	Note
Horizontal	Display Period	t _{HV}	-	480	-	tclk	1920/4
	Blank	t _{HB}	40	70	200	tclk	
	Total	t _{HP}	520	550	680	tclk	
Vertical	Display Period	t _{VV}	-	1080	-	Lines	
	Blank	t _{VB}	228	270	300	Lines	
	Total	t _{VP}	1308	1350	1380	Lines	

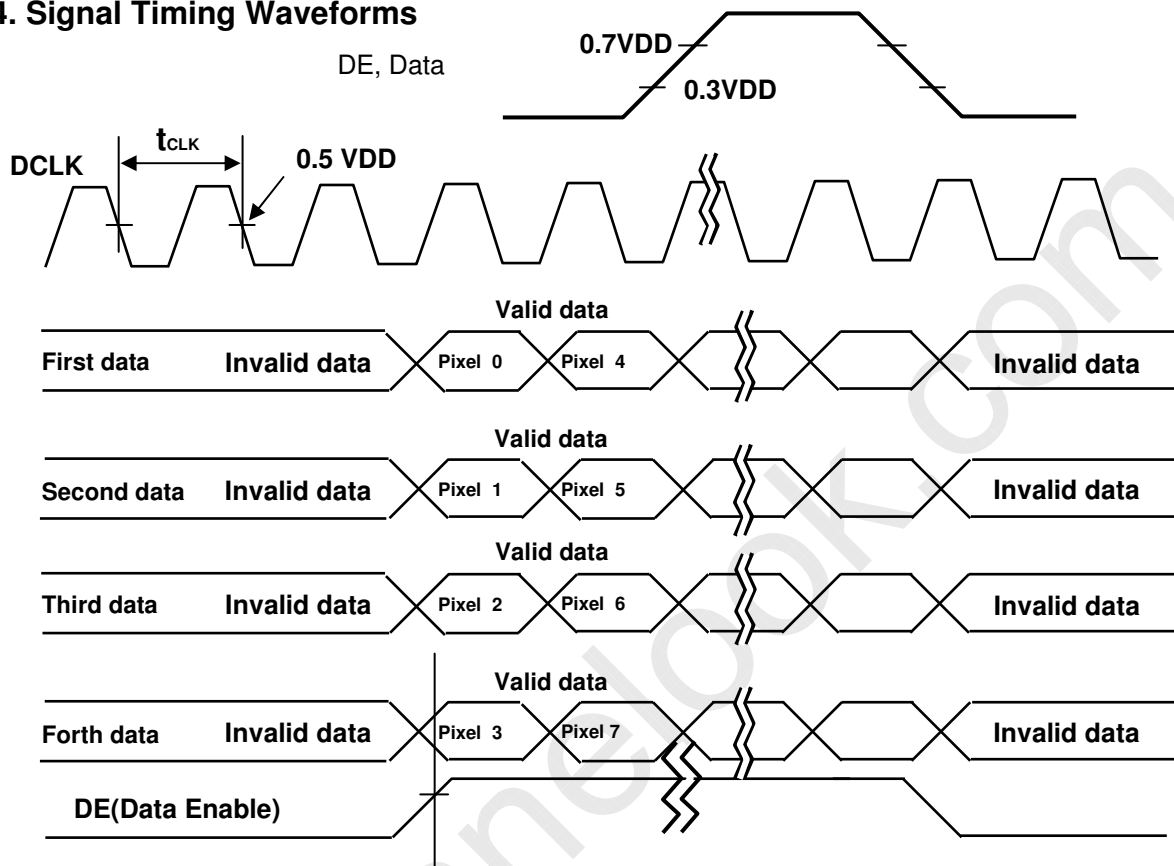
ITEM		Symbol	Min	Typ	Max	Unit	Note
Frequency	DCLK	f _{CLK}	66.97	74.25	75.00	MHz	
	Horizontal	f _H	121.8	135	140	KHz	
	Vertical	f _V	95	100	103.7	Hz	

Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode).
The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

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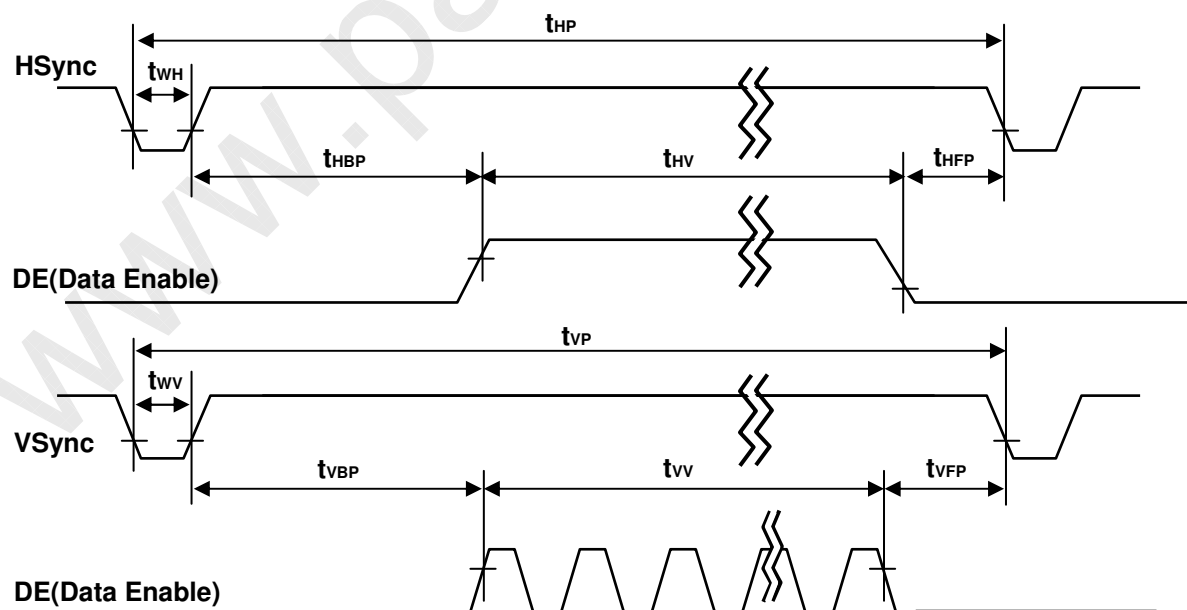
3-4. Signal Timing Waveforms



* Reference : Sync. Relation

$$* t_{HB} = t_{HFP} + t_{WH} + t_{HBP}$$

$$* t_{VB} = t_{VFP} + t_{WV} + t_{VBP}$$





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3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

Table 8. COLOR DATA REFERENCE

Color		Input Color Data																													
		RED										GREEN										BLUE									
		MSB					LSB					MSB					LSB					MSB					LSB				
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	RED (1019)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1020~1023)	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	GREEN (1019)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	GREEN (1020~1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
BLUE	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0

	BLUE (1019)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1020~1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

LC420WUF

Product Specification

3-6. Power Sequence

3-6-1. LCD Driving circuit

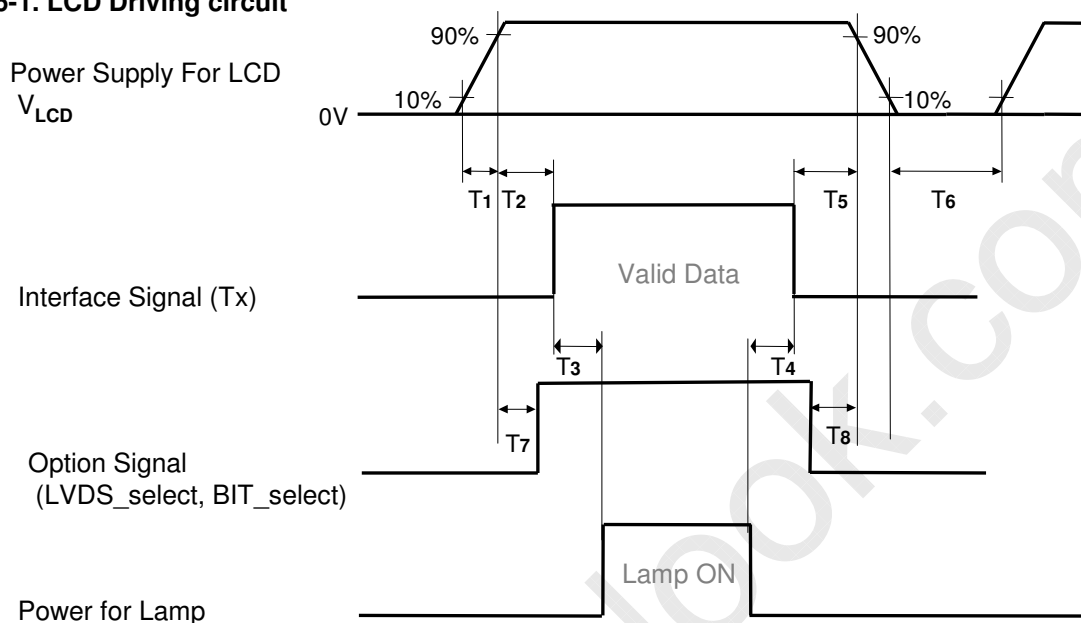


Table 9. POWER SEQUENCE

Parameter	Value			Unit	Notes
	Min	Typ	Max		
T1	0.5	-	20	ms	
T2	0.5	-	$3 \times (1/f_v)$	ms	3,5
T3	200	-	-	ms	4
T4	200	-	-	ms	4
T5	0	-	-	ms	3,5
T6	2.0	-	-	s	2,6
T7	0	-	T2	ms	5
T8	0	-	-	ms	5

- Note :
1. Please avoid floating state of interface signal at invalid period.
 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
 3. The case when the T2/T5 exceed $3 \times (1/f_v)$, it operates protection pattern (Black pattern) till valid signal inputted. There is no reliability problem. (ex. 60Hz : $3 \times (1/60\text{Hz}) = 50\text{ms}$)
 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 5. If the on time of signals (Interface signal and Option signals) precedes the on time of Power (V_{LCD}), check the LCD logic Power (V_{cc}) is under 0.8V, otherwise it will be happened abnormal display.
 6. T6 should be measured after the Module has been fully discharged between power off and on period.

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Product Specification

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm 2^{\circ}\text{C}$. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

FIG. 1 shows additional information concerning the measurement equipment and method.

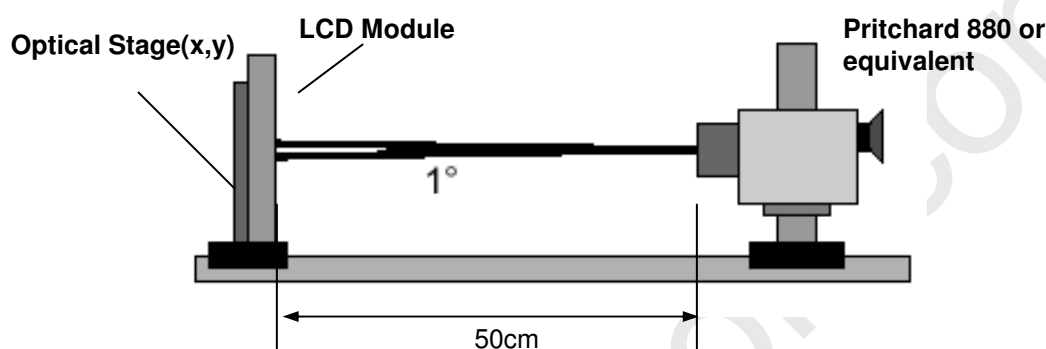


FIG. 1 Optical Characteristic Measurement Equipment and Method

$T_a = 25\pm 2^{\circ}\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 120\text{Hz}$, $D_{\text{clk}} = 74.25\text{MHz}$, $V_{\text{BR_A}} = 1.6\text{V}$, $V_{\text{BR_B}} = 3.3\text{V}$

Table 13. OPTICAL CHARACTERISTICS

Parameter		Symbol		Value			Unit	Note
				Min	Typ	Max		
Contrast Ratio		CR		900	1300	-		1
Surface Luminance, white		L _{WH}		360	450	-	cd/m²	2
Luminance Variation		δ _{WHITE}	5P	-	-	1.3		3
Response Time	Gray-to-Gray	G to G		-	8	12	ms	4,5
	MPRT			-	8	10		
Color Coordinates [CIE1931]	RED	Rx		Typ -0.03	0.640	Typ +0.03		
		Ry			0.335			
	GREEN	Gx			0.289			
		Gy			0.610			
	BLUE	Bx			0.144			
		By			0.066			
	WHITE	Wx			0.279			
		Wy			0.292			
Viewing Angle (CR>10)								
	x axis, right(φ=0°)	θr		89	-	-	degree	6
	x axis, left (φ=180°)	θl		89	-	-		
	y axis, up (φ=90°)	θu		89	-	-		
	y axis, down (φ=270°)	θd		89	-	-		
Gray Scale				-	-	-		7

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Product Specification

Notes :1. Contrast Ratio(CR) is defined mathematically as :

CR(Contrast Ratio) = Maximum CR_n (n=1, 2, 3, 4, 5)

$$CR_n = \frac{\text{Surface Luminance at position n with all white pixels}}{\text{Surface Luminance at position n with all black pixels}}$$

n = the Position number(1, 2, 3, 4, 5). For more information, see FIG. 2.

2. Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at $25 \pm 2^\circ\text{C}$. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white.

For more information see the FIG. 2.

3. The variation in surface luminance, δ WHITE is defined as :

$$\delta \text{ WHITE}(5P) = \text{Maximum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}) / \text{Minimum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5})$$

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .

For more information, see the FIG. 2.

4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)

※ G to G Spec is average of measured time.

5. MPRT is defined as the 10% to 90% blur-edge width B_{ij} (pixels) and scroll speed U (pixels/frame)at the moving picture. For more information, see FIG 4

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 5.

7. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 14.

Table 14. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
10bit	[Normal]
L0	0.10
L63	0.29
L127	1.06
L191	2.50
L255	4.69
L319	7.66
L383	11.5
L447	16.1
L511	21.6
L575	28.1
L639	35.4
L703	43.7
L767	53.0
L831	63.2
L895	74.5
L959	86.7
L1023	100

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Product Specification

Measuring point for surface luminance & measuring point for luminance variation.

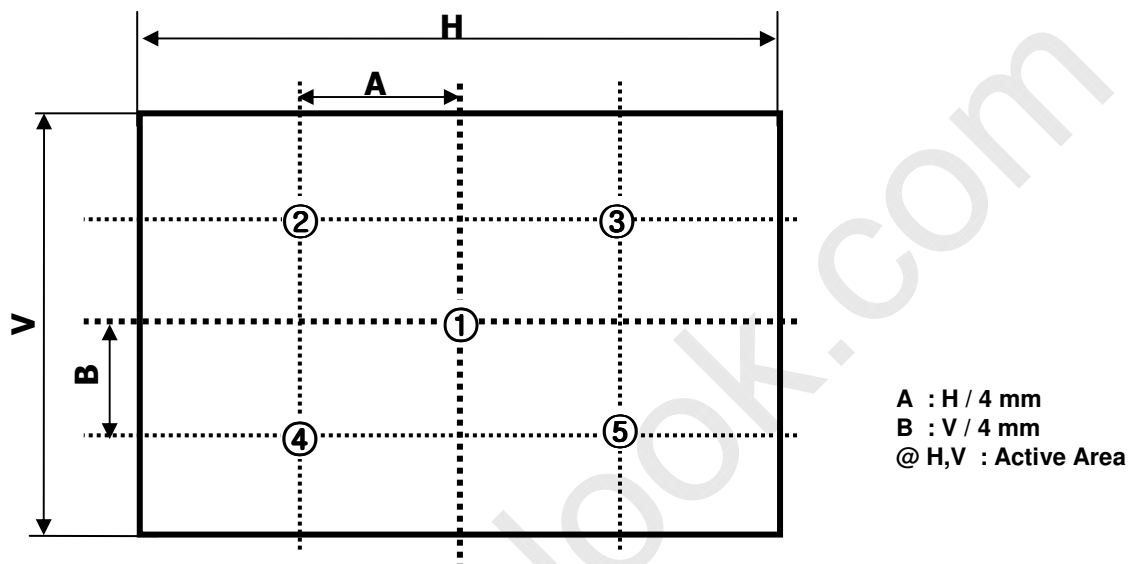


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

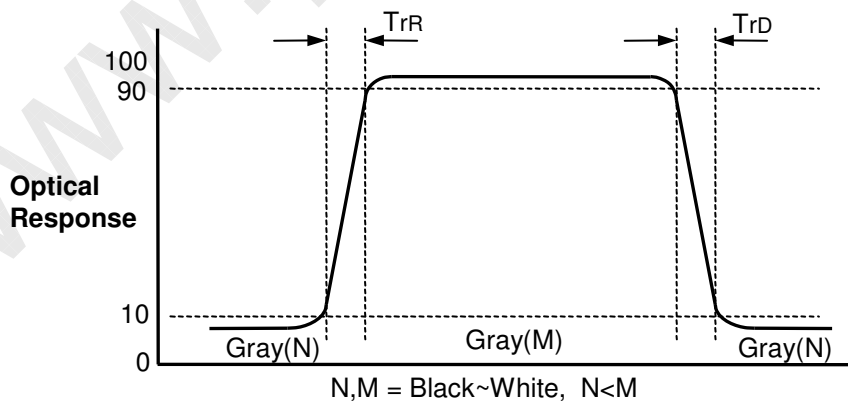


FIG. 3 Response Time

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Product Specification

MPRT is defined as the 10% to 90% blur-edge with B_{ij} (pixels) and scroll speed U (pixels/frame) at the moving picture.

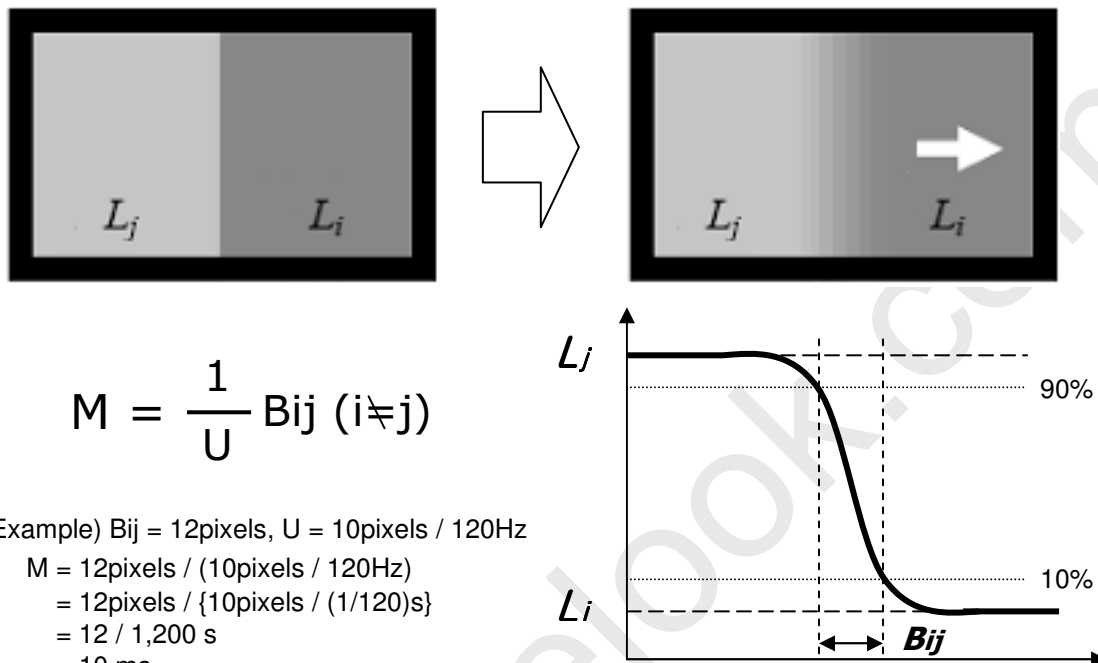


FIG. 4 MPRT

Dimension of viewing angle range

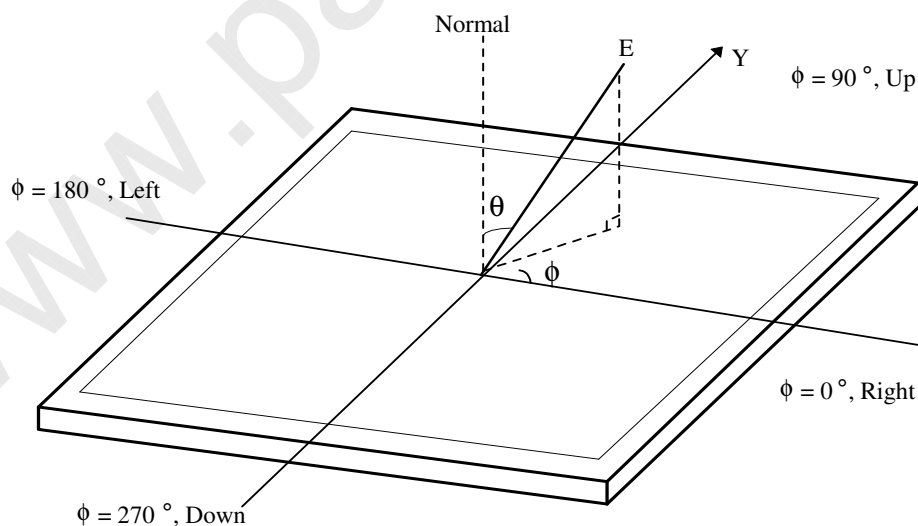


FIG. 5 Viewing Angle



LC420WUF

Product Specification

5. Mechanical Characteristics

Table 15 provides general mechanical characteristics.

Table 15. MECHANICAL CHARACTERISTICS

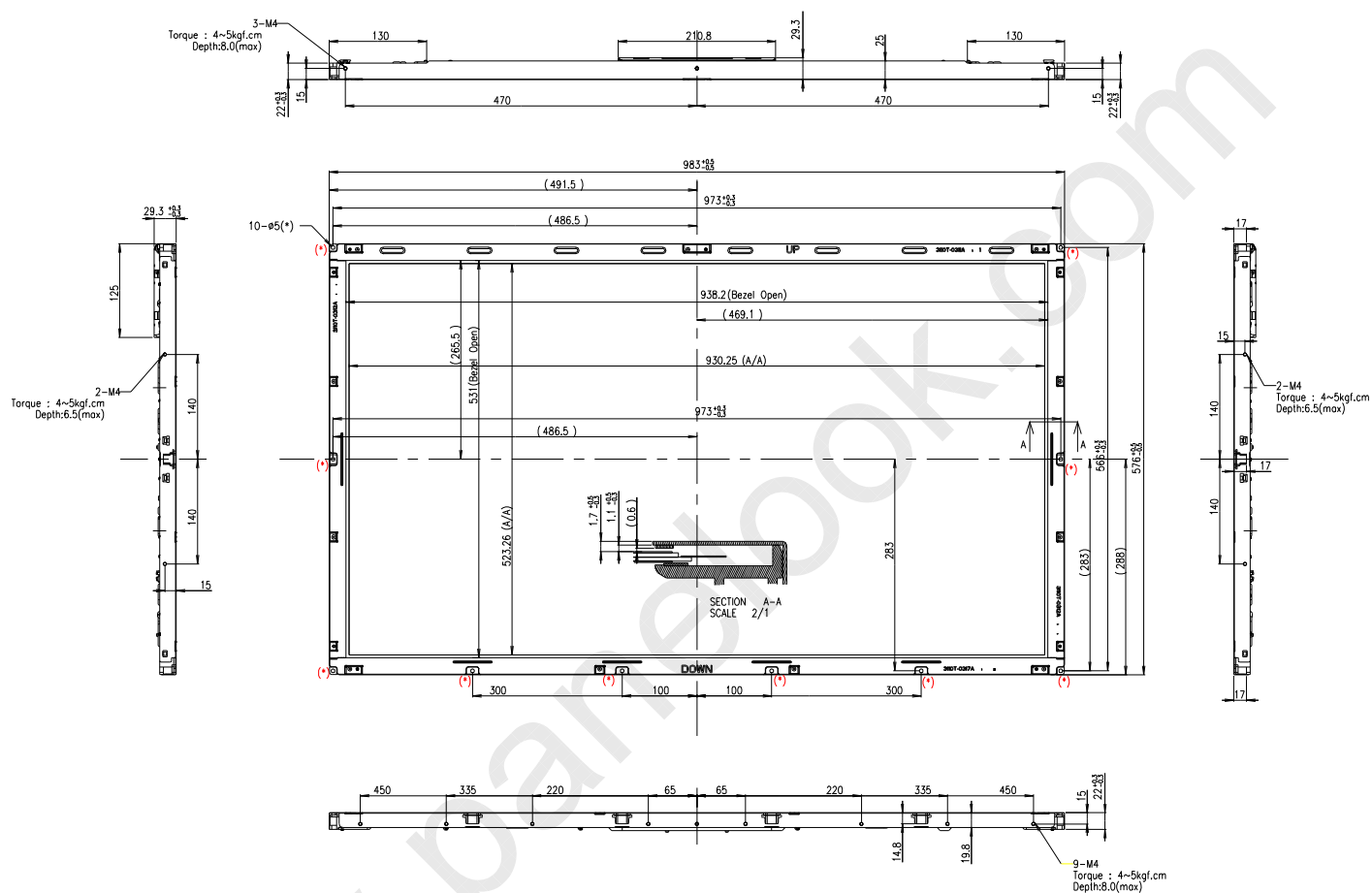
Item	Value	
Outline Dimension	Horizontal	983.0 mm
	Vertical	576.0 mm
	Depth	29.3 mm
Bezel Area	Horizontal	938.2 mm
	Vertical	531.0 mm
Active Display Area	Horizontal	930.25 mm
	Vertical	523.26 mm
Weight	9.8 Kg (Typ.) , 10.5Kg (Max.)	

Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

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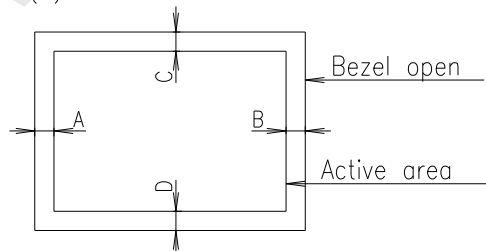
Product Specification

<FRONT VIEW>



NOTES

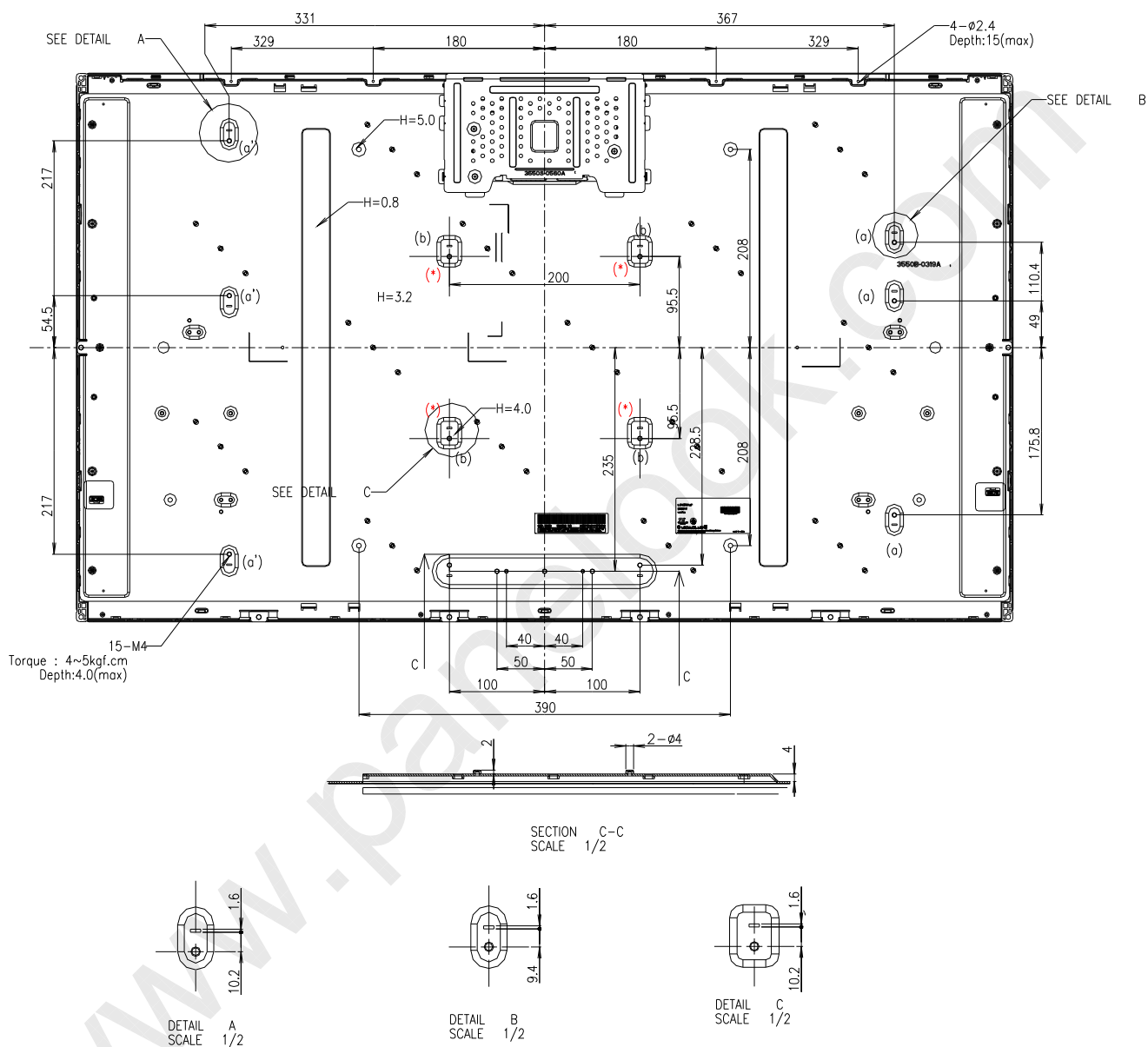
1. Unspecified tolerances are to be $\pm 1.0\text{mm}$.
2. '*' marks stand for the points must be assembled to the SET.
3. Tilt and partial disposition tolerance of display area are as following.
 - (1) X-Direction : $IA-BI \leq 1.5\text{mm}$
 - (2) Y-Direction : $IC-DI \leq 1.5\text{mm}$



LC420WUF

Product Specification

<REAR VIEW>



NOTES

1. Unspecified tolerances are to be $\pm 1.0\text{mm}$.
2. '*' marks stand for the points must be assembled to the SET.

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Product Specification

6. Reliability

Table 16. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min One time each direction
6	Shock test (non-operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : ± X, ± Y, ± Z One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.



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Product Specification

7. International Standards

7-1. Safety

- a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
- b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
- c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus..

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHz to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)



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Product Specification

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH)

D : YEAR

F : PANEL CODE

H : ASSEMBLY CODE

E : MONTH

G : FACTORY CODE

I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	A	B	C

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module.

This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one Pallet : 15 pcs

b) Pallet Size : 1140 mm X 990 mm X 820 mm.



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Product Specification

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
(if not, it can cause conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic



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Product Specification

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

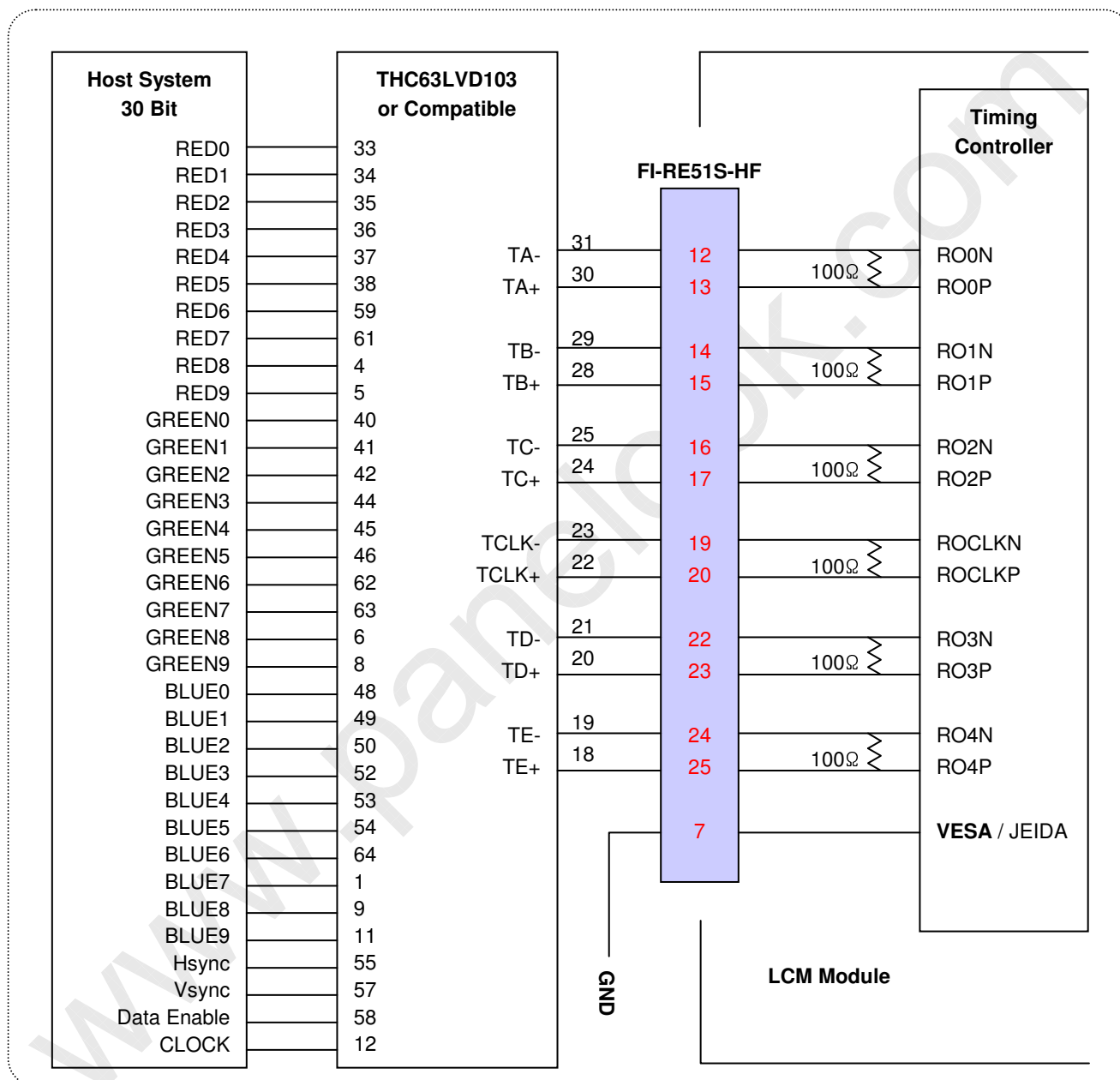
- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer.
This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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Product Specification

APPENDIX-I-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L")



Notes:

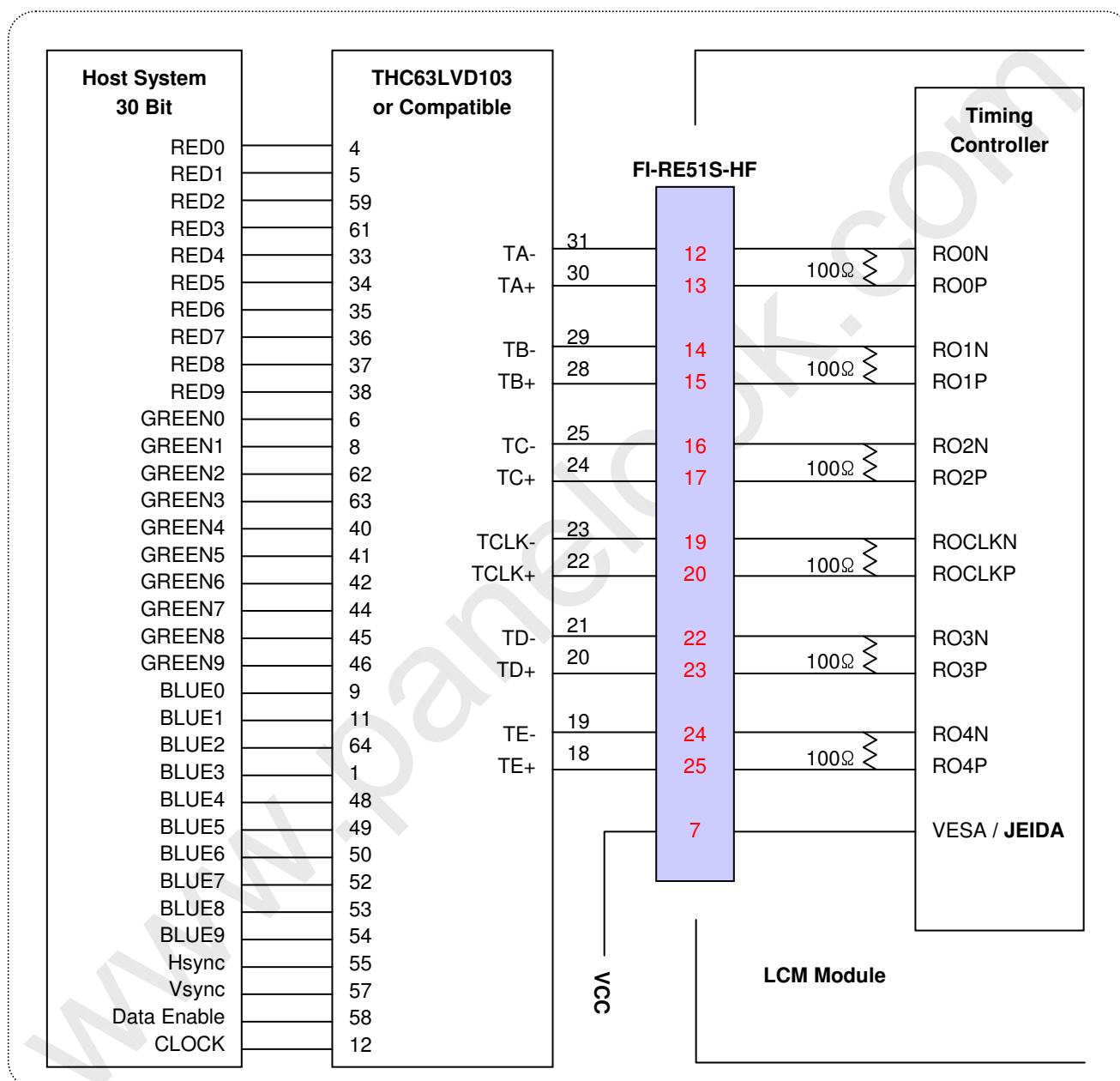
1. The LCD module uses a 100 Ohm(Ω) resistor between positive and negative lines of each receiver input.
2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
3. '7' means MSB and '0' means LSB at R,G,B pixel data.

LC420WUF

Product Specification

APPENDIX-I-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")



Notes:

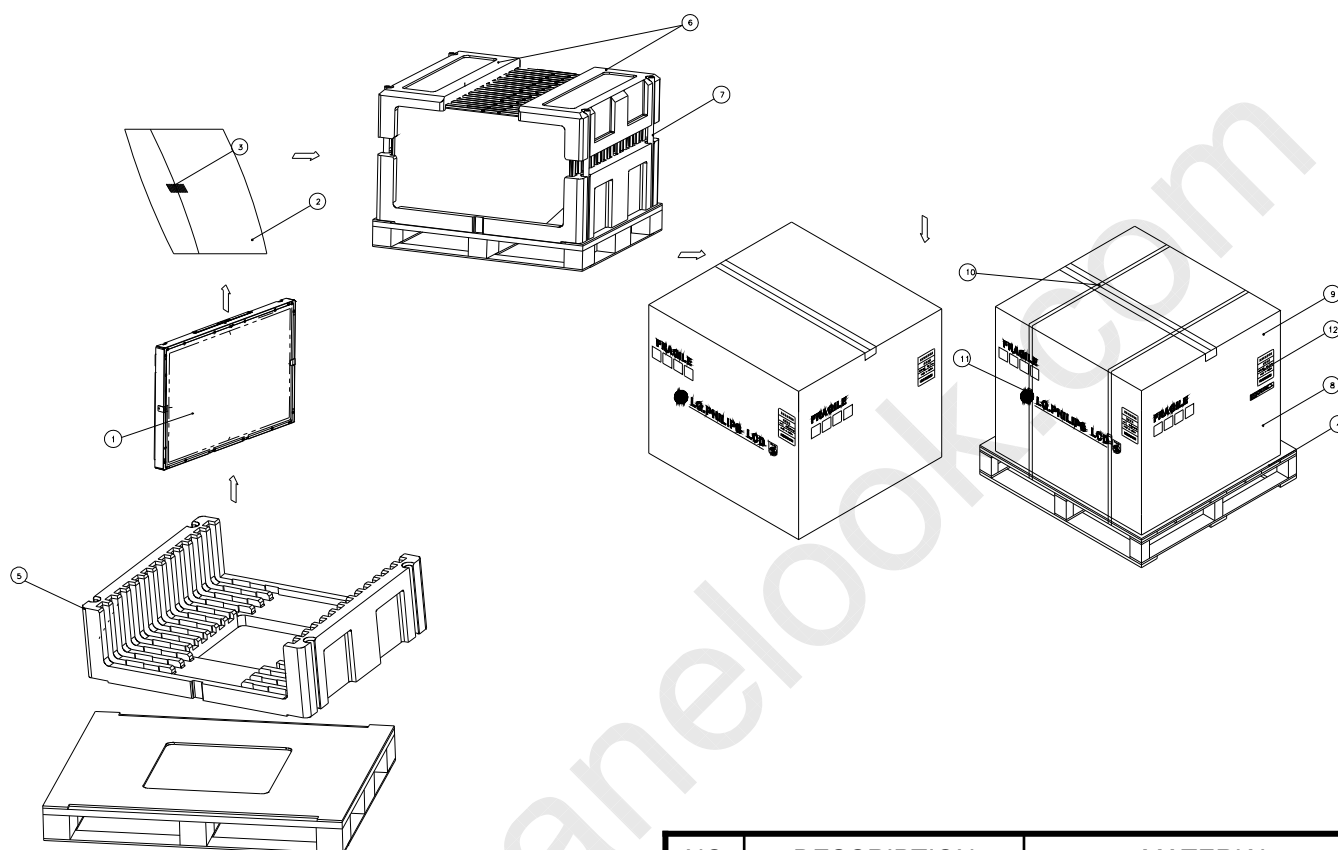
1. The LCD module uses a 100 Ohm(Ω) resistor between positive and negative lines of each receiver input.
2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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Product Specification

APPENDIX-II

■ Pallet Ass'y



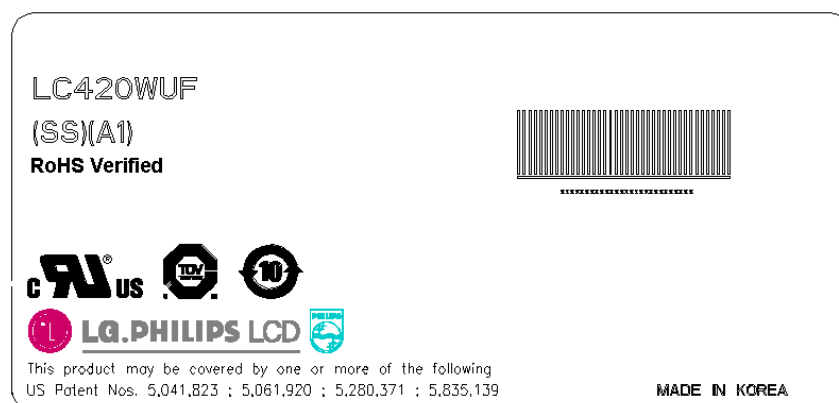
NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	42INCH
3	TAPE	MASKING TAPE
4	PALLET	PAPER 1140X990X130MM
5	PACKING,BOTTOM	EPS
6	PACKING, TOP	EPS
7	ANGLE,POST	PAPER
8	ANGLE,PACKING	PAPER
9	BAND,CLIP	STEEL
10	BAND	PP
11	LABEL	YUPO 80G 100X100

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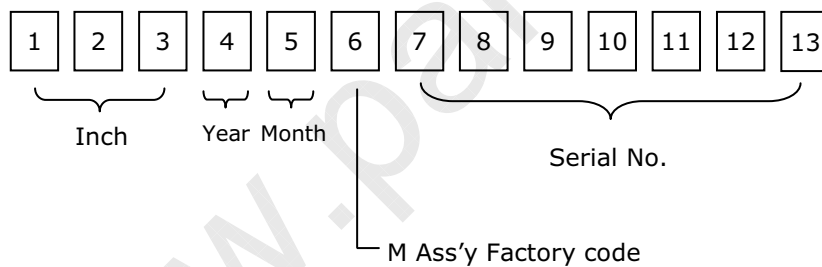
Product Specification

APPENDIX- III

■ LCM Label



■ Serial No. (See CAS 26page for more information)

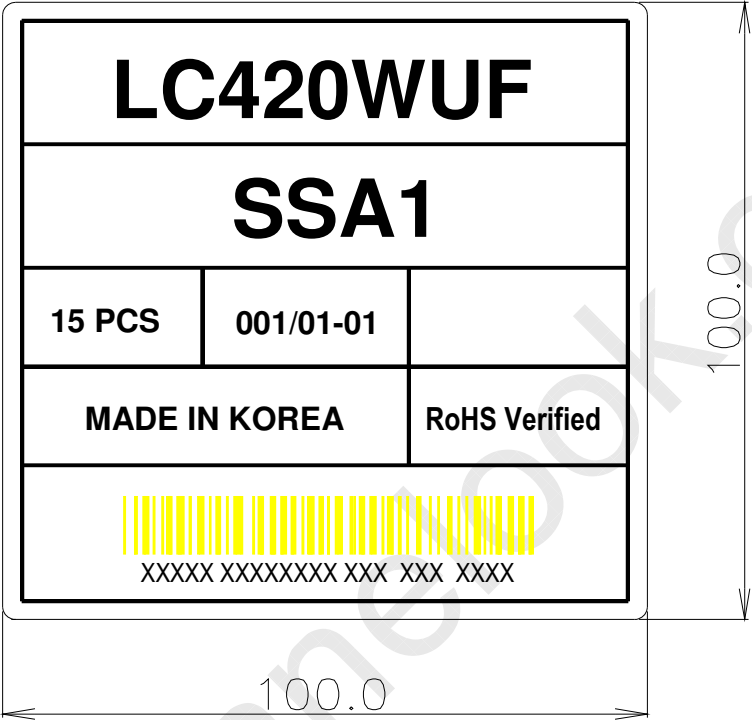


LC420WUF

Product Specification

APPENDIX- IV

■ Pallet Label



LC420WUF

Product Specification

APPENDIX- V

1. Lamp electrical Spec.

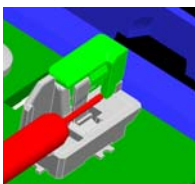
No	Item	Code	Unit	Variational Characteristics		Remark
				Input Voltage	Characteristics	
1	Lamp Voltage	VL	Vrms	20V	1580 \pm 10% (3.0mA) 1400 \pm 10% (6.0mA) 1310 \pm 10% (8.0mA)	
2	Lamp Power (Reference value)	WL	W	20V	4.7W (3.0mA) 8.4W (6.0mA) 10.5W (8.0mA)	
3	Brightness	B	Cd/m ²	20V	8,000 \pm 10% Cd/m ² (3.0mA) 21,500 \pm 10% Cd/m ² (6.0mA) 25,500 \pm 10% Cd/m ² (8.0mA)	
4	Lamp Current	IL	mA _{rms}	7 mA _{rms}		
5	Established starting Voltage	Es	Vrms	1790 Vrms (25℃) 2330 Vrms (0℃)		
6	Effective Length	L _E	mm	Lamp center \pm 455 Min		
7	Chromaticity	x		0.237 \pm 0.01		
		y		0.200 \pm 0.01		
8	Peak spectrum (Reference value)		nm	Red	610	
				Green	545	
				Blue	450	
9	Discharge Stabilization time	Ts	min	3 Max		
10	Operating Frequency	Fr	KHz	40 ~ 80		
11	Discharge time lag of a set	TD	sec	1.0 Max		

2. Lamp type : CCFL

3. Number of CCFL : 20 pcs

4. Balance Capacitor Specification : 22pF \pm 1.1pF

5. Structure Of Balance PCB & Lamp



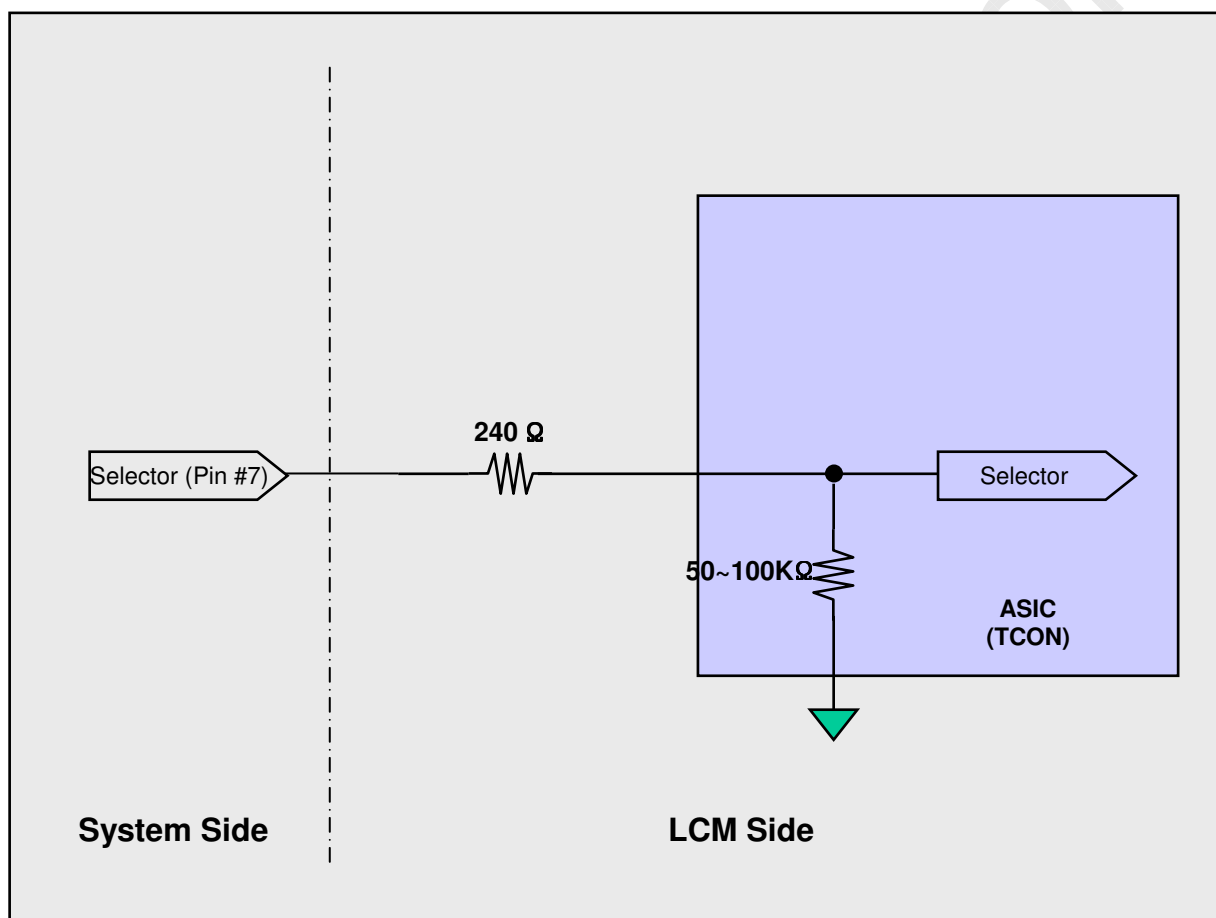
LC420WUF

Product Specification

APPENDIX- VI

■ Option Pin Circuit Block Diagram

Circuit Block Diagram of LVDS Format Selection pin



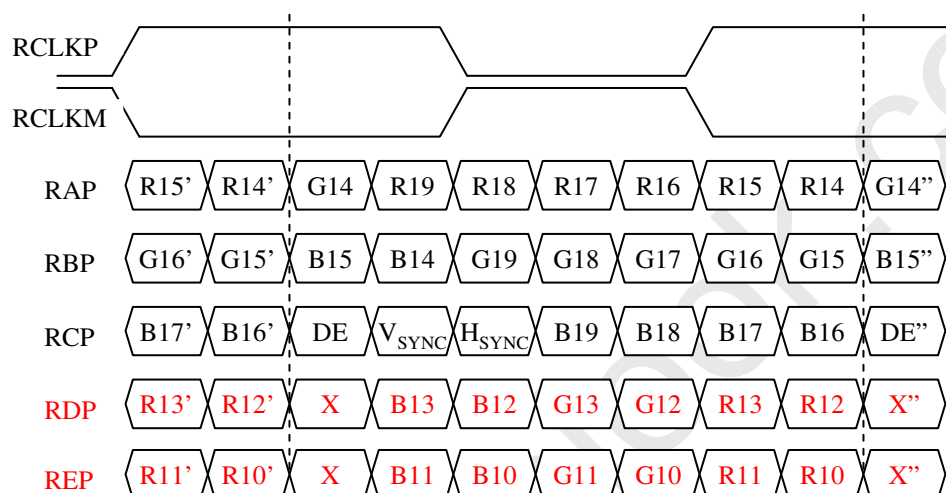
LC420WUF

Product Specification

APPENDIX- VII

LVDS Data-Mapping info. (10bit)

■ LVDS Select : “H” Data-Mapping (JEIDA format)



■ LVDS Select : “L” Data-Mapping (VESA format)

